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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant(s): Eduard K. de Jong
Assignee: Sun Microsystems, Inc.
Title: MULTIPLE INSTRUCTION DISPATCH TABLES FOR
APPLICATION PROGRAM OBFUSCATION
Serial No.: 10/672,183 Filing Date: September 25, 2003
Patent No.: 7,353,499 Issued: April 1, 2008
Examiner: Qing Chen Group Art Unit: 2191
Docket No.: SUN040024

Monterey, CA
August 19, 2008

ATTENTION: CERTIFICATE OF CORRECTIONS BRANCH
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Certificate
AUG 27 2008
of Correction

REQUEST FOR ENTRY OF
CERTIFICATE OF CORRECTION

Sir:

Please enter the enclosed Certificate of Correction (PTO Form 1050) in the above patent.

The errors sought to be corrected were made by

☒ The Patent and Trademark Office as explained below. Thus, no fee is required for the Certificate of Correction pursuant to 37 CFR §1.322.

☐ Applicant(s) (at least in part). See next section for explanation. This appropriate fee under 37 CFR §1.323 has been authorized below.

Attached as Exhibit A (1 page) is the relevant page of the Amendment dated August 29, 2007, which supports the requested correction to Claim 29, and shows that the error was made by the U.S. Patent and Trademark Office.

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As shown in Exhibit A, Lines 4 to 5 of originally numbered Claim 32 (renumbered in the Patent as Claim 29) read "...over said transformed application program code". The same lines in the patent at Column 33, Lines 64 to 65, read "...over said transform&d application program code".

Applicants respectfully request entry of the enclosed Certificate of Correction. The Commissioner is hereby authorized to charge any fees required for consideration and entry of the enclosed documents, and to credit any overpayment of fees to Deposit Account No. 50-0553.

Please direct all inquiries concerning this request to the undersigned attorney.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 19, 2008.



Attorney for Applicant(s)

August 19, 2008

Date of Signature

Respectfully submitted,



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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,353,499

Page 1 of 1

APPLICATION NO. : 10/672,183

ISSUE DATE : April 1, 2008

INVENTOR(S) : Eduard K. de Jong

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 33, Claim 29, Line 64, replace "transform&d" with --transformed--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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a processor; and

a memory, coupled to said processor, having stored therein computer readable instructions wherein executing said computer readable instructions on said processor provides:

means for reading an application program comprising code;

means for determining a plurality of dispatch tables associated with said application program;

means for transforming said application program into application program code configured to utilize said plurality of dispatch tables during application program execution to determine the location of instruction implementation methods to be executed based at least on using a current instruction counter value to select a dispatch table in said plurality of dispatch tables for use with an application program instruction corresponding to said current instruction counter value; and

means for sending said application program code.

31. (Previously Presented) The apparatus of claim 30 wherein said means for determining further comprises means for determining the encoding of said plurality of dispatch tables based at least on a relative frequency of instructions in said application program code.

32. (Original) The apparatus of claim 30 wherein said means for determining further comprises filtering said plurality of dispatch tables to flatten the frequency distribution of instructions over said transformed application program code.

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33. (Original) The apparatus of claim 30 wherein

Exhibit A